ARTHUR CAMPOS

SOFTWARE ENGINNER

afcampos.dev@gmail.com
437-422-4357
linkedin.com/in/afcampos
github.com/Afcam
afcampos.dev

EXPERIENCE

Junior Journeyman Electrician | Freelance

OCT 2021 - SEP 2022, UNAÍ, MG / BRAZIL

- Conducted electrical problem identification and diagnosis, ensuring compliance with safety standards such as NBR-5410.
- Installed and replaced faulty switches, sockets, plugs, and fuses in residential settings, consistently receiving positive feedback from clients.

Electronic Engineering Intern | Finatec

SEP 2019 - JUN 2020, BRASILIA, DF / BRAZIL

- Designed satellite communication hardware modules using VHDL and embedded C, implementing DVB-RCS2 and DVB-S2 protocols.
- Developed over 30 IP prototype modules, performing integration, unit tests, simulations, and deployments, ensuring successful validation. Utilized Logic Analyzer, AXI-JTAG, UART, and Git for effective testing and project management.

EDUCATION

BrainStation | Diploma, Software Engineering

APR 2023 - JUN 2023, TORONTO, ON / CANADA

University of Brasilia | Bachelors in Science, Electronic Engineer

MAR 2016 - AUG 2021, BRASILIA, DF / BRAZIL

PROJECTS

Author | GitClash

2023, BRAINSTATION CAPSTONE

- A browser card game that incorporates the Git flow and Git commands, creating a fun and educational experience for players.

Author | Encoding Modules of DVB-RCS2

2021, UNIVERSITY FINAL THESIS

- Designed and developed the encoding modulator for the DVB-RCS2 protocol using VHDL and C models
- Conducted hardware-in-the-loop tests and utilized Vivado, SDK, and Xilinx Zedboard for synthesis and validation

Member | Irrigation System

2021, UNIVERSITY INTERDISCIPLINARY PROJECT (PI2)

- Collaborated in building an automated and portable autonomous irrigation system using Raspberry Pi and C++ language
- Implemented wireless solar modules for optimizing irrigation efficiency based on data

SKILLS

Programming:
JavaScript/TypeScript, React.js, HTML, CSS/SCSS, Node.js, Express.js, SQL, C/C++, VHDL, Python

Platforms:
Linux, Windows

Hardware:
Raspberry Pi, ESP32, ATMEGA, Zedboard, Basys3, MSP430

Spoken Languages:
English & Portuguese

PROFILE

Passionate software engineer with a background in electronic engineering. Experienced in designing and developing hardware modules for satellite communication protocols. Currently pursuing a software engineering diploma to enhance full-stack development skills. Committed to continuous learning and creating user-friendly solutions that drive industry growth and innovation